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Frank Vahid is the author of Digital Design with RTL  
Design, VHDL, and Verilog, 2nd Edition, published by  
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~~Chapter 2: Combinational Logic Design~~

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Vahid Dargahi's 71 research works with 764 citations and 9,175 reads, including: Phase-Disposition PWM Based Active Voltage Control of Seven- Level Nested Neutral-Point-Piloted (NNPP) Inverters

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## ~~Chapter 5: Register Transfer Level (RTL) Design~~

This book introduces a modern approach to embedded system design, presenting software design and hardware design in a unified manner. It covers trends and challenges, introduces the design and use of single-purpose processors ("hardware") and general-purpose processors ("software"), describes memories and buses, illustrates hardware/software tradeoffs using a digital camera example, and ...

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